

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,933,207 B2
APPLICATION NO. : 10/829213
DATED : August 23, 2005
INVENTOR(S) : Tran et al.

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page, in the Abstract, line 13, please delete "ideally equal" after "cells is".

Title Page, in the Abstract, lines 14-21, please delete the following:

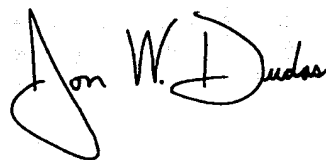
"and is defined as equal to one-half of minimum pitch, with minimum pitch being defined as equal to the smallest distance of a line width plus width of a space immediately adjacent said line on one side of said line between said line and a next adjacent line in a repeated pattern within the array. The respective area is preferably no greater than about $7F^2$, and most preferably no greater than about $6F^2$."

Formal Drawing Sheet 2 of 2, please delete "34" per the annotated and replacement drawing sheets attached.

Col. 7, line 17, please delete "silicon" after "within the" and insert --semiconductor--.

Signed and Sealed this

Fifteenth Day of August, 2006

A handwritten signature in black ink, reading "Jon W. Dudas". The signature is stylized, with a large loop for the "J" and a cursive "Dudas".

JON W. DUDAS
Director of the United States Patent and Trademark Office

